

- 📁 Drafts
- 📁 Pending
- 📁 Active
 - 🔍 L2: {4035} request\$4 near9 retr\$3
 - 🔍 L3: {137} resourc\$2 with 2
- 📁 Failed
- 📁 Saved
 - 🔍 S1: {147} [{"5257399"} or {"5671365"} or {"5710972"} or {"5267235"} or {"6449652"}]
 - 🔍 S2: {124} maximum near9 pending near9 request\$3
 - 🔍 S4: {0} S2 with S3
 - 🔍 S5: {0} S2 and S3
 - 🔍 S3: {28} live near9 lock near9 request
 - 🔍 S6: {147} request and S1
 - 🔍 S7: {1} [{"5941972"}].PN.
 - 🔍 S8: {1} [{"6421753"}].PN.
 - 🔍 S9: {1} [{"6425035"}].PN.
 - 🔍 S10: {1} [{"6736854"}].PN.
- 📁 Favorites
- 📁 Tagged {18}
- 📁 UDC
- 📁 Queue
- 📁 Trash

[illegible]

	U	P	Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Retrieval Class	Ir
1	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 5379379 A	19950103	24	Memory control unit with selective ex	711/3	711/159		Becker, Rob
2	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 6092162 A	20000718	8	Register access controller for preventi	711/163	711/103		Chiang, Joh
3	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 6916945 B2	20041109	16	Quiesce system; storage device and m	711/135	711/145		Harris, Kenne
4	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 6574689 B1	20030603	6	Method and apparatus for live-lock pr	710/52	710/112		Zaidi, Nazar
5	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 5931924 A	19990803	12	Method and system for controlling ac	710/41	710/116		Armilli, Ravi
6	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 5935234 A	19990810	12	Method and system for controlling ac	710/244	710/113		Armilli, Ravi
7	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 6029219 A	20000222	18	Arbitration circuit for arbitrating reques	710/111	710/116		Michitono, T
8	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 20040225755 A1	20041111	7	Method and apparatus for avoiding fl	710/1			Quach, Tuan
9	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 20030037071 A1	20030220	17	Quiesce system; storage device and m	707/200			Harris, Kenne
10	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 6078981 A	20000620	8	Live lock prevention method in multipr				BACHAND, G
11	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 6898678 B1	20050524	16	Shared memory with programmable siz	711/151	710/107		Six, Laurent e
12	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 20040078520 A1	20040422	14	Disk array storage device with means	711/114			Dori, Arif e
13	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 5745915 A	19980428	44	System for parallel reading and proces	711/114	711/157		Cooper, Tho
14	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 20030146183 A1	20030724	31	Systems, methods, and computer prog	710/6			Goode, Dav
15	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 5937205 A	19990810	21	Dynamic queue prioritizallor, by modify	710/6	710/41		Mattison, RIC
16	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 6738881 B1	20040518	25	Multichannel DMA with scheduled po	711/168	370/329		Olivier, Ger
17	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 20030145210 A1	20030731	25	Method, system, program, and data str	711/182			Taylor, Julian
18	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 6487643 B1	20021126	13	Method and apparatus for preventing	711/150	711/148		Khare, Man

$711/141 - 159$
 $163 - 169$
 $710/16, 22-24, 32-35, 52-58$
 $718/101 - 105, 118$



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